

Faculty Profile

Name: **Dr. P. Pavan Kumar**
Designation: Assistant Professor
Teaching Areas: Programming with C, Object oriented programming
Distributed Systems, Data Structures, Design and analysis
of algorithms, Computer Organization and Advanced
Computer Architecture
Research Interests: Computer Vision, Deep Learning, Multi-core Architecture,
Real-time systems
Education: Ph.D., Computer Science and Engineering, JNTU,
Anantapur, 2019
M.Tech., Computer Science and Engineering, National
Institute of Technology, Trichy, India, 2011
B.Tech., Information Technology, ACET, JNTU Hyderabad,
2008



Research / Selected Publications:

1. **P. Pavan Kumar**, Gomathy V, Anna Devi E, Shweta Sankhwar, Lakshmanaprabu SK, "An Intelligent COVID-19 Classification Model using Optimal Gray-Level Co-Occurrence Matrix Features with Extreme Learning Machine". International Journal of Computer Applications in Technology, ISSN: 1741-5047, Special Issue, 2020.
2. B. Balakiruthigaa, **P.Pavan Kumar**, P. Deepalakshmia, Sachi Nandan Mohanty, Deepak Guptac, K. Shankar "Segment routing based energy aware routing for software defined data center", Cognitive Systems Research,, ISSN: 1389-0417, Vol. 64, pp. 146–163, 2020.
3. Jatindra Kumar Dash, Gandham Girish, **P. Pavan Kumar**, E. Sudarshan, Achyuth Sarkar "Classification of Lung Tissue Patterns on HRCT Images: Nature of Region of Interest and Classifier Performance", International Journal of Control and Automation, ISSN: 2005-4297, VOL. 13, NO. 4, pp. 1184-1196, 2020.
4. Sudarshan E., K. Seena Naik and **P. Pavan Kumar**, "Parallel approach for backward coding of wavelet trees with CUDA", ARPJ Journal of Engineering and Applied Sciences, ISSN: 1819-6608, VOL. 15, NO. 9, pp. 1094-1100, 2020.
5. Dr. E. Laxmilydia, **P.Pavan Kumar**, Vydarsi Sitarama Prasad, Chinmaya "Data Analysis and Prediction of COVID-19 Using Machine Learning Models", International Journal of Advanced Science and Technology, ISSN: 2005-4238, Vol. 29, No. 7, pp. 4512 - 4518, USA, 2020.
6. **P.Pavan Kumar**, Ch. Satyanarayana, A. Ananda Rao and P. Radhika Raju. "Design and Implementation of Task reprocessing on multi-core Platform", Application and Theory of Computer Technology, Archy world Publishing Ltd., Minster Court, Crown Street, Liverpool, Merseyside, UK. 2017.
7. **P. Pavan Kumar**, P. Chakradhar and P. Vijaya kumar. "Design and Implementation of Task reprocessing on multi-core Platform", In the International Journal of Electronics Communication and Computer Engineering, Volume 3, Issue (4), ISSN 2249- 071X, pp. 551-557, Florida, USA. 2018.