

Faculty Profile

Name: **Digvijay V Nair**
Designation: Assistant Professor
Teaching Areas: Control systems, Electrical Sciences, Electromagnetic fields and waves, Microwave Engineering and Signals & Systems
Research Interests: Wide Area control, Controller synthesis and fault tolerant controlsystems, Smart grids
Education: (PhD), JNTU-Hyderabad.
M.Tech (Control Systems), NIT Kurukshetra, 2003
B.E (Electronics & Power) Govt. College of Engg., Amravati University, 1997.



Professional Experience: (Total: 17 years)

1. 2011- Till date: Assistant Professor, Faculty of Science & Technology, IFHE, Hyderabad.
2. 2009-2011: Assistant Professor, BITS Pilani, Goa Campus
3. 2005-2009: Faculty Member, Icfai Tech, Hyderabad
4. 2004-2005: RCERT, Chandrapur, Maharashtra
5. 1998-2001: RCERT, Chandrapur, Maharashtra.
6. 1997-1998: Govt College of Engineering, Chandrapur, Maharashtra

Research Publications:

1. Nair, D.V., Murty, M.S.R. Fault tolerant-based virtual actuator design for wide-area damping control in power system. *ElectrEng* (2020). <https://doi.org/10.1007/s00202-020-01094-4>. Online First. Springer-Verlag. (Science Citation Index, SCIE, Scopus)
2. Nair, D.V., Murty, M. Reconfigurable control as actuator fault-tolerant control design for power oscillation damping. *Prot Control Mod Power Syst* 5, 8 (2020). <https://doi.org/10.1186/s41601-020-0151-3>. Springer (ESCI, Scopus)
3. D. V. Nair and M. S. R. Murty, "Simplified robust damping control design of SVC in power system using LMI approach: Polytopic representation of operating conditions," *2017 International Conference on Technological Advancements in Power and Energy (TAP Energy)*, Kollam, 2017, pp. 1-6, doi: 10.1109/TAPENERGY.2017.8397307
4. D. V. Nair and M. S. R. Murty, "Modal analysis of Power System and study of oscillatory instability," *2016 2nd International Conference on Applied and Theoretical Computing and Communication Technology (iCATccT)*, Bangalore, 2016, pp. 667-672, doi: 10.1109/ICATCCT.2016.7912084.